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Hello

Welcome to this edition of the InnoFour newsletter, an informative document with several interesting topics helping you to understand how to become more productive and efficient in your Electronic Design. The newsletter contains many links to in-depth articles on our website that you can visit by clicking on the ([read more...](#)) links.

InnoFour extends to Scandinavia

In addition to the Benelux area, InnoFour has extended their agreement with Mentor Graphics and Omnify Software to become their representative in the Scandinavian region. InnoFour's local contact is Yehoshua (Joshua) Shoshan (Technical Account Manager Scandinavia).

Yehoshua brings 16 years' experience of ASIC and FPGA design and verification from Ericsson, LSI-Logic, Texas Instruments and EBV-Elektronik. Yehoshua started his career as RTL and test bench designer at Ericsson where he invented a unique method for multi-processor distributed verilog simulation (1993). Since then he has been methodology engineer, ASIC FAE and ASIC technical leader for a number of successful taped-out ASICs. Before InnoFour Yehoshua worked as Altera FAE at EBV-Elektronik.

Yehoshua will work from Stockholm and can be reached at: +46 7303 08150 or y.shoshan@innofour.com

Product news



Exceptional Deal on PADS!

For a limited time, we're offering qualified Altium, P-CAD, and Protel customers a complete PADS ES

Suite for just **€ 1.250,=**

That's less than what you pay Altium for support today.

([read more...](#)) or [contact us](#)

Mentor Graphics & Valor

Mentor Graphics has signed an agreement to acquire Valor. With this acquisition Mentor Graphics will extend its portfolio with a strong set of products focusing on the PCB manufacturing market.

It also completes our solutions with a world leading component library called the Valor Parts Library (VPL) ([read more...](#))

Assertion Based Verification

Assertion Based Verification (ABV) has become within reach to the FPGA designer with the recent introduction of Modelsim DE.

If you are one of the FPGA designers and would like to read more about this methodology, then it is good to check the [Verification Horizons](#) (next issue December) or follow Harry Foster on Twitter starting December ([read more...](#)).

Seminars and Events

Successful PLM Seminar

InnoFour recently hosted a very successful Product Lifecycle Management (PLM) seminar.

Managers and Designers participated in the discussion around PLM and saw how the Omnify Software solution could help them to overcome their PLM challenges. As Luc Bossuyt, COB Application Project Manager at ST-ERICSSON said: *"The solution from Omnify Software seems to provide a solution for every aspect of our day to day business."*

Many of the attendees left the seminar with a better idea on how to tackle their PLM problem, as Luc mentioned: *"I came with a lot of expectations to InnoFour's Product Lifecycle Management*

(PLM) seminar" ...and... "I believe it is fair to say that my expectations for the seminar are met" ([read more...](#))

10/12: Understanding the Basics of OVM Web Seminar

As a verification engineer you have probably already heard of the Open Verification Methodology (OVM) and added "The OVM methodology book" to your reading list. Whether or not you have opened the book, this web seminar presents a tour of the important aspects of the methodology ([read more...](#))

26/11 and 1/12: Xilinx X-Fest

X-fest is a global series of FREE technical seminars, offering practical, how-to training for designers interested in using the

latest FPGA technology. InnoFour will be present on these events as a partner, so if you would like to meet us there please [register](#). The upcoming events are on 26/11 in Antwerpen (BE) and 01/12 in Horsens (DK) ([read more...](#))

Jan 2010: Verificaton Event

InnoFour in cooperation with Mentor Graphics will organize an Advanced Verification event with topics like Assertion Based Verification by [Harry Foster](#) the industry leading verification expert, OVM, code coverage, and SystemVerilog. The event will be held in Almelo (NL) and Kista (SE).

Interested in attending this event? Please [register \(free\)](#) or [check our website](#) for exact dates, locations and agenda.

Training events

Be prepared when times change!

Many organisations struggle with getting projects in these economic challenging times. The local governments have initiated special programs (e.g. "Deeltijd ww") that enable you to use this timeframe for education and get ramped up again for better economic times. Check our [training website](#) on how InnoFour can help you to be ready when new projects come your way!

26/11: The CES Fundamentals Tips & Tricks **NEW!**

The CES Fundamentals, Tips & Tricks course was developed to provide a practical guideline to manage constraints in your design. The fundamental part explains the usage of CES and principles in the Expedition Flow. It also provides basic knowledge on the tasks an electrical engineer needs to meet and how this influences the PCB designers demands ([read more...](#))

New to Assertion Based Verification (ABV)?

If you are new to ABV then a good place to start is the [Verification Academy](#) containing a special module on ABV.

Alternatively you could also attend our [SystemVerilog Assertions \(SVA\) training](#) to learn how to become proficient using SVA for assertion based verification.