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Hello,

Welcome to this edition of the InnoFour newsletter, a document with several interesting topics helping you to understand how to become more productive and efficient in your Electronic Design.

The newsletter contains many interesting topics and links to in-depth articles on our website that you can visit by clicking on the [\(read more...\)](#) links.

Product news



BluePrint Live on YouTube

These informative instructional technical videos will show you how to create a panel drawing, how to create an assembly or fabrication drawing, how to create document templates [\(view...\)](#)

PADS9.2 Available

Several enhancements for PADS 9.2 came directly from customer suggestions on the [Mentor Ideas](#) webpage. Many thanks to all those who posted ideas, voted on others' ideas, and added comments or suggestions to help other users. [\(read more...\)](#)

Get a daily Tip&Trick on Twitter

The InnoFour technical team will try to publish a daily Tip&Trick on Twitter. You can find these Tips & Tricks by following us on Twitter [\(checkout InnoFour on Twitter...\)](#)

New Builds available from DownStream

DownStream updated their software executable with new builds for both BluePrint (v10.2 build 391) and CAM350 (v2.2 build 508). For specifics on each new release. [\(read more...\)](#)

EE7.9 Available

The Expedition Enterprise update 9 is available, containing enhancements and updates, many based directly on customer feedback. With this release, the version number is shortened to "EE 7.9". [\(more details on supportnet...\)](#)

New colleague: Ivonne de With

Hi all, since 1th of February I work at InnoFour BV as a Sales Admin. I come from the dairy industry, so it took some time to grow accustomed to the EDA branch. It is however great to work with such a innovative and creative team.

News and Events

Mentor @ DAC Extended

Most of you have probably not been able to join us at DAC in Anaheim this year. Therefore it is probably good to know that Mentor Graphics has decided to make their successful DAC presentations also available for you in an online event called 'Mentor @ DAC Extended' starting July 13th. [\(read more...\)](#)

6/9: FPGAworld 2010

The FPGAworld Conference addresses all aspects of digital and hardware/software system engineering on FPGA technology. It's a discussion and network forum for researchers and engineers [\(read more...\)](#)



14/9: Meeting the challenges of DDRx Design Workshop

In this workshop, we will cover some of the basic electrical requirements of DDR2/DDR3 interfaces, show you how to meet the timing and signal integrity (SI) requirements of these memory interfaces, and provide tips on developing these memory subsystems. [\(read more...\)](#)

Training events

InnoFour's training schedule can be found on our [website](#).

If you need a training on a topic that is not listed, or would like us to create a custom training, please [let us know](#).

Our upcoming Expedition Enterprise training events are:

6/9-7/9: The CES for Expedition PCB.

This course is designed to cover all the necessary skills required to use CES efficiently and effectively in DxDesigner-Expedition and Design Capture/Design View-Expedition flows. [\(read more...\)](#)

15/9-17/9: DxDesigner for Expedition PCB Flow

Using the DxDesigner tools suite, you will gain proficiency in project management with Dashboard, schematic capture with DxDesigner, part selection using DxDatabook and much more. [\(read more...\)](#)

28/9-30/9: Expedition PCB Advanced.

This course is developed to help you understand many of the advanced layout options available for Expedition PCB including many of the optional utilities. [\(read more...\)](#)

3/11 The CES Fundamentals Tips & Tricks

This course was developed to provide a practical guideline through the constrained process of your design. The fundamental part explains the usage of CES and principles in the Expedition Flow. [\(read more...\)](#)

20/12-23-12: Expedition PCB Introduction

This course presents the workflow and methods of PCB layout. From fundamental library concepts, to the PCB editor environment and the PCB layout process. [\(read more...\)](#)